



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
DUTTA, et al.

Atty. Docket

US 018181

Serial No. 10/005,551

Filed: Nov. 8, 2001

Title: High Speed Computation in Arithmetic Logic Circuit

Commissioner for Patents
Washington, D.C. 20231

LETTER TO OFFICIAL DRAFTSMAN

Sir:

Enclosed are formal drawings for filing in the
above-identified application.

Respectfully submitted,

By
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited this date with the United States Postal Service as
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2121#2
Formal
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